



#6 / Amdt A  
4-17-02  
JL

PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Hirokazu MIYAZAKI

CONFIRMATION NO. 8771

Serial No. 09/788,596

GROUP 2811

Filed February 21, 2001

Examiner N. Parekh

MOUNTING STRUCTURE OF  
SEMICONDUCTOR DEVICE AND  
MOUNTING METHOD THEREOF

RECEIVED  
APR 11 2002  
TECHNOLOGY CENTER 2800

AMENDMENT

Commissioner for Patents

Washington, D.C. 20231

Sir:

Responsive to the Official Action of March 28, 2002,  
please amend the above-identified application as follows:

IN THE CLAIMS:

✓  
Cancel claims 10, 11, 15 and 16.

REMARKS

Responsive to the requirement for restriction,  
applicant elects Group I, claims 1-9 and 12-14, drawn to a  
semiconductor device, without traverse.

Non-elected method claims 10, 11, 15 and 16 are  
cancelled herewith, but may be made the subject of a divisional  
application to be filed at any time during the pendency of the  
present application.